Product data sheet

1. General description

NPN switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: PMBT3906

2. Features and benefits

- Collector current capability I_C = 200 mA
- Collector-emitter voltage V_{CEO} = 40 V
- AEC-Q101 qualified

3. Applications

· General switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	200	mA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 10 mA	[1]	100	-	300	

^[1] Pulsed test: $t_p \le 300 \,\mu s$; $\delta \le 0.02$

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	Е	emitter		j
3	С	collector		В —
			1 2 SOT23	E sym021



40 V, 200 mA NPN switching transistor

6. Ordering information

Table 3. Ordering information

ype number Package						
	Name	Description	Version			
PMBT3904		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMBT3904	%1A

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	200	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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10. Characteristics

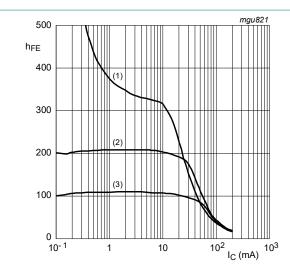
Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A		-	-	50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A		-	-	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 0.1 mA	[1]	60	-	-	
		V _{CE} = 1 V; I _C = 1 mA	[1]	80	-	-	
		V _{CE} = 1 V; I _C = 10 mA	[1]	100	-	300	
		V _{CE} = 1 V; I _C = 50 mA	[1]	60	-	-	
		V _{CE} = 1 V; I _C = 100 mA	[1]	30	-	-	
OLSat	collector-emitter	I _C = 10 mA; I _B = 1 mA		-	-	200	mV
	saturation voltage	I _C = 50 mA; I _B = 5 mA		-	-	300	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA; I _B = 1 mA		650	-	850	mV
	voltage	I _C = 50 mA; I _B = 5 mA		-	-	950	mV
t _d	delay time	I _C = 10 mA; I _{Bon} = 1 mA; I _{Boff} = -1 mA		-	-	35	ns
t _r	rise time			-	-	35	ns
t _s	storage time			-	-	200	ns
t _f	fall time			-	-	50	ns
C _c	collector capacitance	$V_{CB} = 5 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	-	4	pF
C _e	emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz		-	-	8	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 10 mA; f = 100 MHz		300	-	-	MHz
NF	noise figure	V_{CE} = 5 V; I_{C} = 100 μA; R_{S} = 1 kΩ; f = 10 Hz to 15.7 kHz		-	-	5	dB

^[1] Pulsed test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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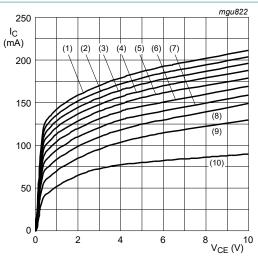


V_{CE} = 1 V (1) T_{amb} = 150 °C

(2) T_{amb} = 25 °C

(3) $T_{amb} = -55 \, ^{\circ}C$

Fig. 1. DC current gain as a function of collector current; typical values



 T_{amb} = 25 °C (1) I_B = 5.5 mA

 $(2) I_B = 5.0 \text{ mA}$

 $(3) I_B = 4.5 \text{ mA}$

 $(4) I_B = 3.5 \text{ mA}$

 $(5) I_B = 3.0 \text{ mA}$

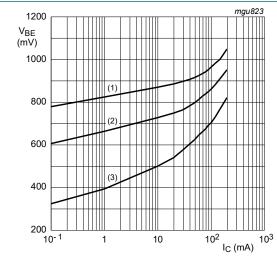
(6) $I_B = 2.5 \text{ mA}$

 $(7) I_B = 2.0 \text{ mA}$ $(8) I_B = 1.5 \text{ mA}$

(9) $I_B = 1.0 \text{ mA}$

 $(10) I_B = 0.5 mA$

Fig. 2. Collector current as a function of collectoremitter voltage; typical values

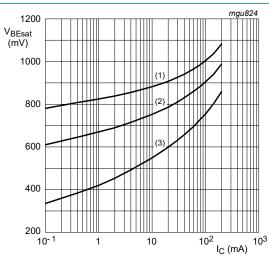


 $V_{CE} = 1 V$

 $(1) T_{amb} = -55 °C$

(2) T_{amb} = 25 °C (3) T_{amb} = 150 °C

Fig. 3. Base-emitter voltage as a function of collector current; typical values



 $I_C/I_B = 10$

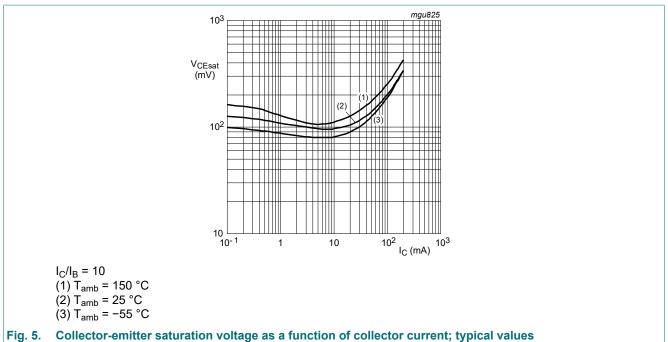
(1) $T_{amb} = -55 \, ^{\circ}C$

 $(2) T_{amb} = 25 °C$

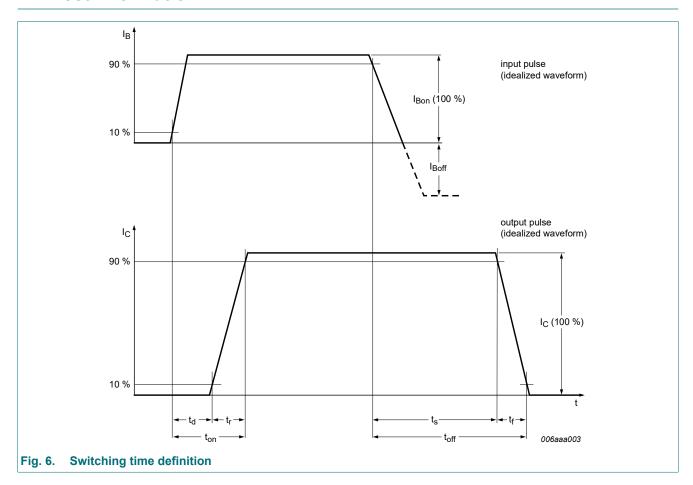
(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values

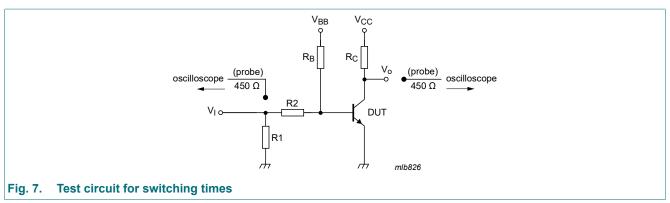
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11. Test information



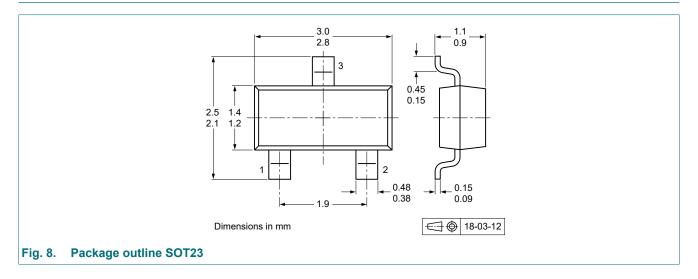
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Quality information

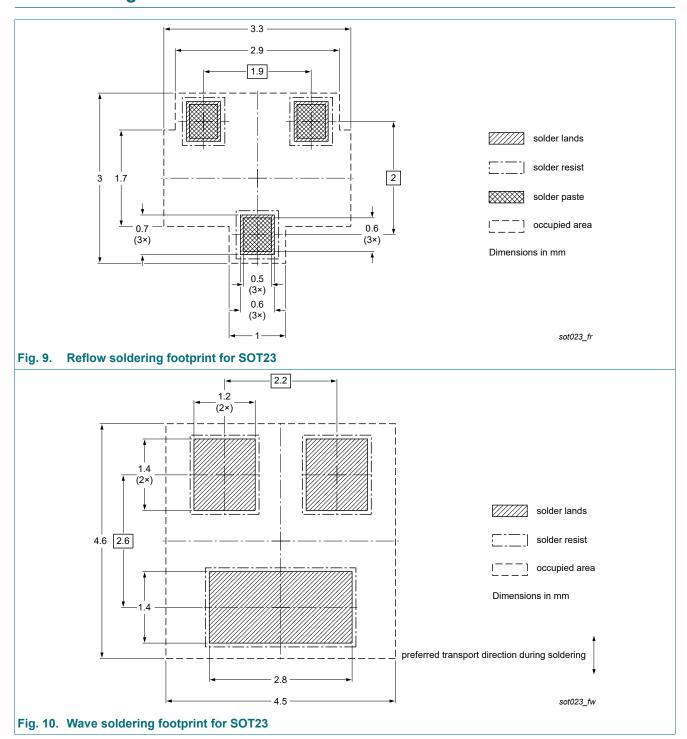
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Table 6. Revision history								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMBT3904 v.4	20230419	Product data sheet	-	PMBT3904 v.3				
Modifications:		 Data sheet changed to automotive qualification in sections Features and benefits, Test information and Legal informations. 						
PMBT3904 v.3	20201105	Product data sheet	-	PMBT3904 v.2				
PMBT3904 v.2	20040112	Product data sheet	-	PMBT3904 v.1				
PMBT3904 v.1	19990427	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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